

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L10	135	(710/41.ccls. 719/321,327.ccls. 709/220.ccls.) and L9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 12:51
L9	9284	(ethernet) near3 (processor card occupant board PCB slot blade PWB)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 12:51
L8	13	(710/41.ccls. 719/321,327.ccls. 709/220.ccls.) and L7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 12:51
L7	5675	((CDP) (cisco adj discovery adj protocol))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 12:49
L2	2933	(710/41.ccls. 719/321,327.ccls. 709/220.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 12:49
S87	293	(discover\$5 identify\$5 poll\$5) near6 (chassis)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 12:34
S86	133	S83 and (discover\$5 near3 Protocol)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 09:20
S83	9284	(ethernet) near3 (processor card occupant board PCB slot blade PWB)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 09:14
S85	2203	(ethernet) near3 (processor board PCB slot blade PWB)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 09:13
S84	9284	(ethernet) near3 (processor card board PCB slot blade PWB)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 09:13

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S82	1	(modular near3 chasis)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 09:10
S81	5	("4660141"   "5313595"   "5428752"   "5761697"   "6092138").PN	US-PGPUB; USPAT; USOCR	OR	ON	2006/04/27 09:10
S80	108	(chassis) and (slot blade cards PCB PWB NIC) and (discover\$5) near4 (protocol)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 08:36
S76	85	(CPCI) with (configur\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 08:35
S79	16	("4692918"   "4918589"   "5018137"   "5060228"   "5088032"   "5144622"   "5155808"   "5161192"   "5179554"   "5226120"   "5261044"   "5301303"   "5319644"   "5497463"   "5522042"   "5764982").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/04/27 07:27
S78	104	((CDP near4 protocol)(cisco adj discovery adj protocol))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 15:21
S72	5669	(CDP (cisco adj discovery adj protocol))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 15:20
S77	2	"6898705".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 15:14
S75	2	"5109484".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 15:13
S74	5	"6701429".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 15:12
S73	58	((cisco adj discovery adj protocol))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 15:12
S57	3	(ethernet near3 switch) near5 (card PWB PCB slot) and (CDP (cisco adj discovery adj protocol))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 14:59

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S71	83	(discover\$5) near6 (algorithm) near15 (system)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 14:57
S70	0	(bootp PXE) near5 (discover\$5) near6 (algorithm)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 14:57
S64	0	(bootp PXE) near5 (ethernet near3 switch) same (backplane) and (address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 14:56
S69	16	("4665514"   "4771425"   "4819228"   "4903261"   "5524110"   "5526353"   "5604741"   "6157649"   "6181681"   "6205149"   "6216167"   "6275499"   "6426952"   "6456597"   "6577628"   "6747995").PN	US-PGPUB; USPAT; USOCR	OR	ON	2006/04/26 14:49
S68	9	(discover\$5) near5 (internal rack cage shelf) near6 (slots card PWB PCB blade)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 14:45
S67	2352	(discover\$5) near5 (slots card PWB PCB blade)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 14:45
S66	0	(discover\$5) near5 (ethernet near3 switch) near5 (slots card PWB PCB blade)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 14:45
S65	5	(bootp PXE) near5 (ethernet near3 switch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 14:44
S63	91	(ethernet near3 switch) same (backplane) and (address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 14:28
S62	139	(ethernet near3 switch) same (backplane)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 14:17
S54	186	(ethernet near3 switch) near5 (card PWB PCB slot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 14:16

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S61	14	(ethernet near3 switch) near5 (assign\$5 near4 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 13:58
S60	0	(ethernet near3 switch) near5 (card PWB PCB slot) near5 (assign\$5 near4 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 13:58
S58	2	(ethernet near3 switch) near5 (card PWB PCB slot) and (discovery adj protocol)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 13:42
S56	80	(ethernet adj switch) near2 (card PWB PCB slot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 13:39
S55	116	(ethernet adj switch) near5 (card PWB PCB slot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 13:39
S53	2	"6415314".pn	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/26 13:37

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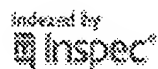
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- ☐ 4. **Design and implementation of IPC network in ATM switching system**  
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- ☐ **19. A server array approach for video-on-demand service on local area network**  
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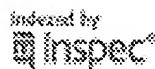
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### 1 [Topology discovery for large ethernet networks](#)



Bruce Lowekamp, David O'Hallaron, Thomas Gross

 August 2001 **ACM SIGCOMM Computer Communication Review , Proceedings of the 2001 conference on Applications, technologies, architectures, and protocols for computer communications SIGCOMM '01**, Volume 31 Issue 4

Publisher: ACM Press

 Full text available: [pdf\(144.05 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Accurate network topology information is important for both network management and application performance prediction. Most topology discovery research has focused on wide-area networks and examined topology only at the IP router level, ignoring the need for LAN topology information. Recent work has demonstrated that bridged Ethernet topology can be determined using standard SNMP MIBs; however, these algorithms require each bridge to learn about all other bridges in the network. Our approach to ...

### 2 [Using the NREN testbed to prototype a high-performance multicast application](#)



Marjory J. Johnson, Matthew Chew Spence, Lawrence Chao

 January 1999 **Proceedings of the 1999 ACM/IEEE conference on Supercomputing (CDROM)**

Publisher: ACM Press

 Full text available: [pdf\(76.92 KB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)

### 3 [Focus on Cisco switch management](#)



Gilbert Haj Held

 December 1999 **International Journal of Network Management**, Volume 9 Issue 6

Publisher: John Wiley &amp; Sons, Inc.

 Full text available: [pdf\(374.42 KB\)](#) Additional Information: [full citation](#), [index terms](#)

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